Date Mailed: November 1, 2001





Sheet 1 of 1

FORM 1449*

INFORMATION DE LOS LIBERT ATEMENT

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number: Application Number: 8688.223US01 09/800,412

Applicant: Yu-Chiang CHENG

Filing Date: 03/06/2001 Group Art Unit: 2841

		U	S. PATENT DOCUMEN	TS				
EXAMINER INITIAL	DOCUMENT NO.	D. DATE NAME CLASS SU		SUBCLASS	FILING DATE IF APPROPRIATE			
								
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	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION		
						YES	NO	
THA								
	ОТН	IER DOCUMENTS	(Including Author, Title,	Date, Pertinent Pag	es, Etc.)			
JHA	U.S. Patent	Application Serial	No. 09/799,900, MULTI-L	AYER CIRCUIT E	OARD			
THA	U.S. Patent	U.S. Patent application Serial No. 09/800,215, MULTI-LAYER CIRCUIT BOARD						
THA	U.S. Patent	U.S. Patent Application Serial No. 09/800,0408, MULTI-LAYER CIRCUIT BOARD						
	†	U.S. Patent Application Serial No. 09/800,409, MULTI-LAYER CIRCUIT BOARD						

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DATE CONSIDERED

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.